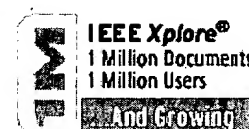


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**1. Spurious effects induced by current switching in power strips of wafer package***Chilo, J.; Fort, P.;*

Components, Hybrids, and Manufacturing Technology, IEEE Transactions on [also IEEE Trans. on Components, Packaging, and Manufacturing Technology, I A, B, C], Volume: 13, Issue: 4, Dec. 1990

Pages: 1110 - 1114

[\[Abstract\]](#)[\[PDF Full-Text \(312 KB\)\]](#)

IEEE JNL

**2. Variation of kink power with cavity length in weakly index guided semiconductor lasers***van der Poel, C.J.; Schemmann, M.F.C.; Acket, G.A.;*

Semiconductor Laser Conference, 1994., 14th IEEE International, 19-23 Sept 1994

Pages: 241 - 242

[\[Abstract\]](#)[\[PDF Full-Text \(120 KB\)\]](#)

IEEE CNF

**3. Parasitic effects induced by power strips current switching in full wafer package***Chilo, J.; Angenieux, G.;*

Microwave Symposium Digest, 1989., IEEE MTT-S International, 13-15 June 1989

Pages: 1259 - 1262 vol.3

[\[Abstract\]](#)[\[PDF Full-Text \(112 KB\)\]](#)

IEEE CNF

**4. Studies on the high-temperature superconductor (HTS)/metal/poly dielectric interconnect structure for packaging applications**

*Paik, K.W.; Mogro-Campero, A.;*  
Components, Packaging, and Manufacturing Technology, Part B: Advanced  
Packaging, IEEE Transactions on [see also Components, Hybrids, and  
Manufacturing Technology, IEEE Transactions on] , Volume: 17 , Issue: 3 , Au  
1994  
Pages:435 - 441

[\[Abstract\]](#) [\[PDF Full-Text \(732 KB\)\]](#) **IEEE JNL**

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**5 Yield improvement by reducing charge-up damage of double polysili  
capacitors during via etch**

*Beugin, V.; Richard, M.;*  
Plasma- and Process-Induced Damage, 2003 8th International Symposium , 2  
April 2003  
Pages:85 - 88

[\[Abstract\]](#) [\[PDF Full-Text \(629 KB\)\]](#) **IEEE CNF**

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**6 A numerical investigation of conjugate heat transfer from a flush he  
source on a conductive board in laminar channel flow**

*Sugavanam, R.; Ortega, A.; Choi, C.Y.;*  
Thermal Phenomena in Electronic Systems, 1994. I-THERM IV. 'Concurrent  
Engineering and Thermal Phenomena', InterSociety Conference on , 4-7 May

Pages:62 - 72

[\[Abstract\]](#) [\[PDF Full-Text \(880 KB\)\]](#) **IEEE CNF**

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**7 Studies on the high temperature superconductor (HTS)/metal/poly  
dielectric interconnect structure for packaging applications**

*Paik, K.W.; Mogro-Campero, A.;*  
Electronic Components and Technology Conference, 1994. Proceedings., 44th  
May 1994  
Pages:362 - 366

[\[Abstract\]](#) [\[PDF Full-Text \(488 KB\)\]](#) **IEEE CNF**

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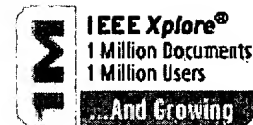
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**1 A 1.3- $\mu$ m wavelength laser with an integrated output power monitor using a directional coupler optical power tap**

Koren, U.; Miller, B.I.; Young, M.G.; Chien, M.; Dreyer, K.; Ben-Michael, R.; C.R.J.;

Photonics Technology Letters, IEEE , Volume: 8 , Issue: 3 , March 1996  
Pages:364 - 366
[\[Abstract\]](#) [\[PDF Full-Text \(240 KB\)\]](#) IEEE JNL

**2 Energy losses in power tap-connectors**

Timsit, R.S.; Sprechel, J.D.;

Transmission &amp; Distribution Construction, Operation &amp; Live-Line Maintenance Proceedings, 1998. ESMO '98. 1998 IEEE 8th International Conference on , 26 April 1998

Pages:285 - 296

[\[Abstract\]](#) [\[PDF Full-Text \(700 KB\)\]](#) IEEE CNF

**3 Polymeric tunable optical attenuator with an optical monitoring tap WDM transmission network**

Sang-Shin Lee; Yong-Sung Jin; Yung-Sung Son; Tae-Kyung Yoo;

Photonics Technology Letters, IEEE , Volume: 11 , Issue: 5 , May 1999  
Pages:590 - 592
[\[Abstract\]](#) [\[PDF Full-Text \(52 KB\)\]](#) IEEE JNL

**4 New fast discrete wavelet**

La Borde, B.;

Time-Frequency and Time-Scale Analysis, 1996., Proceedings of the IEEE-SP

International Symposium on , 18-21 June 1996  
Pages:41 - 44

[\[Abstract\]](#)   [\[PDF Full-Text \(616 KB\)\]](#)   IEEE CNF

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**5 Minimization of prime cost of production of serial products TAP**

*Pisanko, A.V.; Kolotvin, A.V.;*

Electron Devices and Materials, 2001. Proceedings. 2nd Annual Siberian Russian Student Workshop on , 3-7 July 2001

Pages:73

[\[Abstract\]](#)   [\[PDF Full-Text \(92 KB\)\]](#)   IEEE CNF

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Published before July 1996

Terms used **Power strip managing monitoring controlling**

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### 1 [Parallelism in relational data base systems: architectural issues and design approaches](#)

Hamid Pirahesh, C. Mohan, Josephine Cheng, T. S. Liu, Pat Selinger

July 1990 **Proceedings of the second international symposium on Databases in parallel and distributed systems**Full text available: [pdf\(2.50 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

With current systems, some important complex queries may take days to complete because of: (1) the volume of data to be processed, (2) limited aggregate resources. Introducing parallelism addresses the first problem. Cheaper, but powerful computing resources solve the second problem. According to a survey by Brodie,<sup>1</sup> only 10% of computerized data is in data bases. This is an argument for both more variety and volume of data to be moved into data base systems. We conject ...

### 2 [Experiments with digital video playback](#)

Richard Gerber, Ladan Gharai

May 1996 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1996 ACM SIGMETRICS international conference on Measurement and modeling of computer systems**, Volume 24 Issue 1Full text available: [pdf\(1.25 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this paper we describe our experiments on digital video applications, concentrating on the static and dynamic tradeoffs involved in video playback. Our results were extracted from a controlled series of 272 tests, which we ran in three stages. In the first stage of 120 tests, we used a simple player-monitor tool to evaluate the effects of various static parameters: *compression type*, *frame size*, *digitized rate*, *spatial quality* and *keyframe distribution*. The tests were carried out ...

### 3 [Microprocessor applications in the nuclear industry](#)

C. Dwayne Ethiridge

April 1980 **ACM SIGCAS Computers and Society**, Volume 10 Issue 3-4Full text available: [pdf\(986.50 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#)


Microprocessors in the nuclear industry, particularly at the Los Alamos Scientific Laboratory, have been and are being utilized in a wide variety of applications ranging from data acquisition and control for basic physics research to monitoring special nuclear material in

long-term storage. Microprocessor systems have been developed to support weapons diagnostics measurements during underground weapons testing at the Nevada Test Site. Multiple single-component microcomputers are now controlling ...

#### 4 The Integrated Dictionary/Directory System

Frank W. Allen, Mary E. S. Loomis, Michael V. Mannino

June 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 2

Full text available:  [pdf\(2.71 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



#### 5 Pen computing: a technology overview and a vision

André Meyer

July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3

Full text available:  [pdf\(5.14 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine; picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions. Starting with a short historic ...



#### 6 Computer security: a survey

Peter S. Browne

September 1972 **ACM SIGMIS Database**, Volume 4 Issue 3

Full text available:  [pdf\(1.04 MB\)](#)

Additional Information: [full citation](#), [references](#)



#### 7 Exploding the interface: experiences of a CSCW network

John Bowers, Tom Rodden

May 1993 **Proceedings of the SIGCHI conference on Human factors in computing systems**

Full text available:  [pdf\(1.12 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The development of human computer interaction has been dominated by the interface both as a design concept and as an artefact of computer systems. However, recently researchers have been re-examining the role of the interface in the user's interaction with the computer. This paper further examines the notion of the interface in light of the experiences of the authors in establishing a network to support cooperative work. The authors argue that the concept of the single interface which provi ...

**Keywords:** CSCW, cooperative systems, observational studies, organizational effects, user interface models



#### 8 Architecture implications of high-speed I/O for distributed-memory computers

Thomas Gross, Peter Steenkiste

July 1994 **Proceedings of the 8th international conference on Supercomputing**

Full text available:  [pdf\(1.06 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We consider the problem of high-speed I/O for a single application running on multiple nodes of a distributed-memory parallel computer. Our model is that the parallel system is



connected to an I/O system that provides the interface between the internal connections of the parallel system and one or more external connections, such as HIPPI links. We identify two primary operations for this I/O system: scattering data from a high speed link across several lower speed links and gathering data f ...

9 Annotated bibliography of the proceedings of the annual simulation symposium (1968-1991)

Ross A. Gagliano, Martin D. Fraser

April 1992 **Proceedings of the 25th annual symposium on Simulation**

Full text available:  [pdf\(1.45 MB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

10 Program Transformation Systems

H. Partsch, R. Steinbrüggen

September 1983 **ACM Computing Surveys (CSUR)**, Volume 15 Issue 3

Full text available:  [pdf\(3.00 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

11 Illustrative risks to the public in the use of computer systems and related technology

Peter G. Neumann

January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Full text available:  [pdf\(2.54 MB\)](#) Additional Information: [full citation](#)

12 Compiler transformations for high-performance computing

David F. Bacon, Susan L. Graham, Oliver J. Sharp

December 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 4

Full text available:  [pdf\(6.32 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

In the last three decades a large number of compiler transformations for optimizing programs have been implemented. Most optimizations for uniprocessors reduce the number of instructions executed by the program using transformations based on the analysis of scalar quantities and data-flow techniques. In contrast, optimizations for high-performance superscalar, vector, and parallel processors maximize parallelism and memory locality with transformations that rely on tracking the properties o ...

**Keywords:** compilation, dependence analysis, locality, multiprocessors, optimization, parallelism, superscalar processors, vectorization

13 "Topologies"—distributed objects on multicomputers

Karsten Schwan, Win Bo

May 1990 **ACM Transactions on Computer Systems (TOCS)**, Volume 8 Issue 2

Full text available:  [pdf\(3.83 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Application programs written for large-scale multicomputers with interconnection structures known to the programmer (e.g., hypercubes or meshes) use complex communication structures for connecting the applications' parallel tasks. Such structures implement a wide variety of functions, including the exchange of data or control information relevant to the task computations and/or the communications required for task synchronization, message forwarding/filtering under program control, and so o ...

14 An evaluation of redundant arrays of disks using an Amdahl 5890

Peter M. Chen, Garth A. Gibson, Randy H. Katz, David A. Patterson

April 1990 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1990 ACM SIGMETRICS conference on Measurement and modeling of computer systems**, Volume 18 Issue 1

Full text available:  [pdf\(1.45 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Recently we presented several disk array architectures designed to increase the data rate and I/O rate of supercomputing applications, transaction processing, and file systems [Patterson 88]. In this paper we present a hardware performance measurement of two of these architectures, mirroring and rotated parity. We see how throughput for these two architectures is affected by response time requirements, request sizes, and read to write ratios. We find that for applications with large ...



15 Parity declustering for continuous operation in redundant disk arrays

Mark Holland, Garth A. Gibson

September 1992 **ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems**, Volume 27 Issue 9

Full text available:  [pdf\(1.57 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

16 Computational Approaches to Image Understanding

Michael Brady

January 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 1

Full text available:  [pdf\(10.04 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

17 The impact of technological advances on programmable controller s(tutorial session)

Robert P. Collins, William J. Ketelhut

June 1985 **Proceedings of the 22nd ACM/IEEE conference on Design automation**

Full text available:  [pdf\(547.93 KB\)](#)


Additional Information: [full citation](#), [abstract](#), [index terms](#)

An input/output system of intelligent, programmable, solid-state power I/O modules inherently distributed to the point of control through integrated serial communications has been developed to provide a stand-alone, user-configurable I/O module. Design of this special I/O product was driven by the demand for greater diagnostics capability and lower installed costs required by programmable controller users. The product development, involving a new 5,500 gate equivalent VLSI chip and a smart ...

18 A window-based graphics frame store architecture

Richard J. Westmore

October 1988 **ACM Transactions on Graphics (TOG)**, Volume 7 Issue 4

Full text available:  [pdf\(1.02 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)

A proposal for a scalable frame store architecture for a hardware-based window graphics system is described. The architecture is based on a distributed linear array of common elements called microframe stores. Each window and viewport can be independently configured in size and color depth. Unlike the strip-based hardware window systems referenced, this system places no restrictions on the number of transitions that can be accommodated on each scanline. The approach described allows real-time ...



**19 Performance evaluation of extended storage architectures for transaction processing**

Erhard Rahm

June 1992 **ACM SIGMOD Record , Proceedings of the 1992 ACM SIGMOD international conference on Management of data**, Volume 21 Issue 2Full text available:  [pdf\(1.47 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The use of non-volatile semiconductor memory within an extended storage hierarchy promises significant performance improvements for transaction processing. Although page-addressable semiconductor memories like extended memory, solid-state disks and disk caches are commercially available since several years, no detailed investigation of their use for transaction processing has been performed so far. We present a comprehensive simulation study that compares the performance of these storage ty ...

**20 Starting a university microcomputer maintenance program**

Roger N. Addelson, Don M. Wee

September 1986 **Proceedings of the 14th annual ACM SIGUCCS conference on User services: setting the direction**Full text available:  [pdf\(2.73 MB\)](#) Additional Information: [full citation](#), [citations](#), [index terms](#)

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